# Claims

[c1]    What is claimed is:

1.A processor comprising:

a central processing unit (CPU) processing data according to an instruction set;

a data memory storing non-stack data;

a stack memory storing stack data;

a memory address generator producing addresses for accessing the data memory; and

a stack pointer generator producing pointers for accessing the stack memory.

[c2]    2.The processor of claim 1 wherein the processor is a Micro Computer System (MCS) series processor.

[c3]    3.The processor of claim 1 wherein the processor processes an 8-bit instruction set.

[c4]    4.The processor of claim 3 wherein the data memory is 256 bytes.

[c5]    5.The processor of claim 3 wherein the stack memory is 256 bytes.